

PATENT APPLICATION
Docket No.: 9898-270
Client Ref. No.: SS-17885-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jeong-Hoon AHN, et al.

Serial No.: 10/678,531

Examiner: Eckert II, George C.

Filed: October 3, 2003

Group Art Unit: 2815

Confirmation No.: 1358

For: INTEGRATED CIRCUIT CAPACITOR STRUCTURE

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Sir:

This Information Disclosure Statement is submitted:

- ☐ under 37 CFR 1.97 (b); or
(Within three months of filing national application; or date of entry of International application;
or before mailing date of first office action on the merits; or before the mailing of a first Office
Action after the filing of a request for continued examination under §1.114; whichever occurs
last)
- ☒ under 37 CFR 1.97 (c) together with either a:
☐ Statement under 37 CFR 1.97 (e), or
☒ a \$180 fee under 37 CFR 1.17 (p); or
(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)
- ☐ under 37 CFR 1.97 (d) together with:
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☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with
copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s)
may be material to the examination of this application and for which there may be a duty to disclose in accordance
with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A
duplicate copy of this sheet is enclosed.

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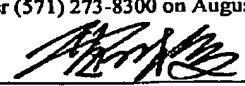
Respectfully submitted,
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08/25/2005 TL0111 00000021 10678531

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Li Mei Vermilya

PATENT APPLICATION

Atty. Docket No.: 9898-270 Serial No. 10/678,531
Applicant: Jeong-Hoon AHN, et al.
Filing Date: October 3, 2003 Group: 2815

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
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FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
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OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
_____	_____	Lin, et al. "Single Mask Metal-Metal-Insulator-Metal (MIM) Capacitor with Copper Damascene Metallization for Sub-0.18 μ m Mixed Mode Signal and System-on-a Chip (SoC) Applications" IEEE 2000, pp.111-113
_____	_____	M. Armacost, et al. "A High Reliability Metal Insulator Metal Capacitor for 0.18 μ m Copper Technology" IEEE 2000, IEDM, pp.157-160

Examiner: _____

Date Considered: _____